

Docket No. 94100419(EP)USC1X1C1D8 PDDD
USSN: 09/776,641

PATENT
Art Unit: 2154

This listing of claims will replace all prior versions, and listings of claims in the application:

LISTING OF CLAIMS:

Claim 1 (currently amended): A pipeline system for decoding data streams of data ~~having portions encoded according to different standards~~ comprising:

a sequence of pipeline stages, at least one of the pipeline stages being reconfigurable ~~to operate according to the different standards;~~

the at least one of the pipeline stages including processing circuitry with an active state which is entered when ~~the data~~ one of said streams of data is received by the at least one of the pipeline stages ~~has~~ with a predetermined activation pattern, ~~the predetermined activation pattern corresponding to one of the different standards; and~~

~~the at least one of the pipeline stages including~~ a state machine having a current state and a previous state; and

wherein only the at least one of the pipeline stages is activated upon recognition of the predetermined activation pattern only upon a predetermined transition of said state machine from the previous state to the current state.

Claim 2 (original): The pipeline system of claim 1, wherein the processing circuitry has an inactive state, in which the at least one of the pipeline stages passes data to a following pipeline stage without processing.

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Claim 3 (original): The pipeline system of claim 1, wherein the sequence of pipeline stages includes at least one spatial decoder stage.

Claim 4 (original): The pipeline system of claim 1, wherein the sequence of pipeline stages includes at least one temporal decoder stage.

Claim 5 (original): The pipeline system of claim 1, wherein the at least one of the pipeline stages is a spatial decoder stage.

Claim 6 (original): The pipeline system of claim 1, wherein the at least one of the pipeline stages is a temporal decoder stage.